

### REMARKS

In the outstanding Official Action, the title of the invention was deemed to be not descriptive, and a new title was required. In response, a new and more descriptive title is provided herewith.

Claim 6 was objected to because of the noted informalities, and claim 6 accordingly has been amended to correct the noted informalities.

On the merits, claims 1-12 were rejected under 35 U.S.C. §102(e) as being anticipated by Fisher et al. for the reasons of record.

In response, independent claims 1 and 8 are herewith amended to add additional novel subject matter, and it is respectfully submitted that claims 1 and 8, and all of the remaining claims depending therefrom, are clearly patentably distinguishable over the cited and applied reference.

Independent claims 1 and 8, as herewith amended, now additionally recite that in case of an interrupt the state of all of the limited subset of hardware resource means are stored within a single clock cycle and all hardware resource means under control of the first instruction set means have their state frozen. It is respectfully submitted that these additional limitations are neither shown nor suggested in Fisher, so that the currently-

pending claims, as herewith amended, are now clearly patentably distinguishable over the cited and applied reference.

In view of the foregoing, it is respectfully submitted that the currently-pending claims are now in condition for allowance, and favorable consideration is earnestly solicited.

Respectfully submitted,

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